

## RESUME

### 1. Personal Information

**NAME** : Abdul Raqib Abdo Asaad ( A.R.A. Asaad ).  
**NATIONALITY** : YEMENI.  
**MARITAL STATUS** : MARRIED WITH CHILDREN.  
**ACADEMIC STATUS** : Professor in Electronics and Computer Engineering.

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### 2 . Degrees Held

- 1) Ph.D Electrical and Electronic Engineering, University of Walse, U.K., 1989.
- 2) M.Sc. Electrical and Computer Engineering( computer Engineering ), University of Miami, U.S.A., 1985.
- 3) B.Sc. ( Honors), Electrical Engineering(Electronics & Communications),University of Alexandria, EGYPT, 1980.

### 3.Experience & Professional career

- 1980 – 1981 : Engineer, Signal Force, Sanaa , Yemen.  
 1981 – 1983 : Engineer, Ministry of Communications, Sanaa, yemen.  
 1983 – 1985 : M.Sc. Student, University of Miami, U.S.A.  
 1985 – 1986 : Lecturer, Dep. Of Electrical Engineering, Faculty of Engineering, Sanaa University, yemen.  
 1986 – 1989 : Ph.D Student, University of Walse, U.K.  
 1989 – 1991 : Academic Advisor, Faculty of Engineering, Sanaa University, Yemen.  
 1989 : Visitor( One Month), Forth-Wagen Institute, West Germany.  
 1989 – 1993 : Assistant Professor, Dep. Of Electrical Engineering, Faculty of Engineering, Sanaa University, Yemen.  
 1989 – 2001 : Lecture, Air defense Faculty, Sanaa, Yemen.  
 1992 : Training( One Month), Lybold Company, West Germany.  
 1991 – 1993 : Vice Chairman, Dep. Of Electrical Engineering, Faculty of Engineering, Sanaa University, Yemen.  
 1993 – 1995 : Chairman, Dep. Of Electrical Engineering, faculty of Engineering, Sanaa University, Yemen.  
 1993 – 1998 : Associate Professor, Dep. Of Electrical Engineering, Faculty of Engineering, Sanaa University, Yemen.  
 1997 : Training ( One Month), Feedback Company, U.K.  
 1998 : Professor, Dep. Of Electrical Engineering ,Faculty of Engineering, Sanaa University, Yemen.  
 1992 – 1998 : Chairman, Computer and Electronics Club, Sanaa, Yemen.  
 1994 – 1997 : Chairman, Dep. Of Computer science and Electrical Engineering, Faculty of Science & Engineering, University of Science and Technology, Sanaa, Yemen.  
 1996 – 1999 : Vice-Dean, Faculty of Engineering, Sanaa University, yemen.  
 1997 – 2002 : Dean, Faculty of Science and Engineering, University of Science and Technology, Sanaa, Yemen.  
 2000 : I attended a course in Laser Engineering, Cairo University.  
 2002 – 2003 : Professor, at Ittihad University, UAE.  
 2003 – 2007 : Dean, Postgraduate studies and Research, University of Science and Technology, sanaa, Yemen.

- 2003 – Present : Member of the academic committee, Faculty of Engineering, Sanaa University, Yemen.
- 2007 – 2009 : Member of the University academic council, Sanaa University, Yemen.

## 4. Membership in Professional Societies

- 1996 – 2007 : IEEE, Member.
- 1998 – 2007 : ACM, Member.
- 1998 – 2005 : LIA, Member.
- 1998 – 2003 : IEICE, Member.
- 1996 – Present : Editor in Chief, JST( Journal of Science and Technology), Sanaa, Yemen.

## 5. Educational Contributions

### 5.1 Teaching Activities

I have taught the following materials:

- 1) Electrical circuits ( DC and AC ).
- 2) Electronic circuits ( DC and AC).
- 3) Digital Electronics.
- 4) Logic circuits ( Analysis & Design ).
- 5) Digital systems design.
- 6) Digital computer organization and design.
- 7) Computer interfacing.
- 8) Advanced digital systems design ( Binary & Ternary Systems ).
- 9) Data Communications.
- 10) Computer Networks.
- 11) Electronic Communications and communication circuits.
- 12) Differential equations.
- 13) Design and supervisor final year graduation projects for Univ. students.
- 14) Supervisor for M.Sc. and Ph.D. students.

## 5.2 Development of Curriculums

- 1) For Electrical Engineering Dep., ( Computer Engineering. & Electronics and Communications engineering ), Faculty of Eng, sanaa Univ.
- 2) For Electrical & Computer science Dep., Faculty of Sci. & Eng., University of Science and Technology, Yemen.
- 3) For Electrical Engineering Dep., Hadramount University, Yemen
- 4) For Computer science Dep., Hadramount University, Yemen.
- 5) For Computer Science & Information systems Dep., Ibrahim alwazier Univ., Yemen.
- 6) For Electronic Eng. & Computer Science Dep., Al-yamania Univ., Yemen.
- 7) For Air Defense Faculty, Sanaa, yemen
- 8) For Electrical Eng. Dep. ( Computer Eng. & Electronics and Communications Eng. ) Al-Hodidah University, Yemen.
- 9) For Ibb University ( Computer Science, Electrical Eng, and Architecture Eng. ), Yemen.

## 6. Supervision For M.Sc. and Ph.D. Students & Examiner until the year 2010

- 1) Number of M.Sc. students which have been graduated and supervised  
By Prof. Dr. ARA Asaad is ( 15 ).
- 2) Number of Ph.D. students which have been graduated and supervised  
By Prof. Dr. ARA Asaad is ( 2 ).
- 3) As an External and Internal Examiner & Committee chairman ( 77 ).

## 7. Studies that have been carried out

- 1) “ An investigation to the energy production and consumption in the Republic Of Yemen until the year 2020”, submitted to the environmental protection Council ( EPC ), Sanaa, Yemen, May, 1999.

- 2) "An investigation to the different energy options that meet the demand and Reduce GHG emission in the Republic Of Yemen until the year 2020 ", Submitted to the EPC, Sanaa, Yemen, July, 1999.
- 3) Carried a joint study on the "assessment of mitigation options in the Republic Of Yemen", Submitted to the EPC, sanaa, Yemen, June, 2000.
- 4) "Updating existing and developing new programs that include measures to Abate GHG Emission", Submitted to the environmental protection authority, sanaa, Yemen, Mars 2010.

## 8. Publications

### 8.1 Scientific Papers

- 1) "Synthesis of Tributary Switching Networks by Spectral means", IEE PROC., VOL.137,Pt.E,NO. 5, 1990, PP. 353 – 358.
- 2) "A systematic method for designing combinational networks based on Linearization techniques", COMCONEL 90 CONFERENCE, Cairo, 1990.
- 3) "A new set of spectral operations", AMSE REVIEW, VOL.16, NO. 4, 1991.
- 4) "A systematic method for P-valued logic design based on the P-valued matrices And linearization techniques", AMSE PRESS, VOL.13, NO.2, 1992, PP.43-64.
- 5) "Minimization of Ternary Reed-Muller Switching functions with fixed polarity", Journal of Engineering, VOL.2, NO.2,1992, PP.37-54.
- 6) "Graphical method for the minimization of Ternary Reed-Muller switching Functions with mixed polarities", Bulletin of the Faculty of Engineering, Assiut University, VOL.20,NO.1, 1992, PP.79-84.
- 7) "Efficient computer method for Ternary Reed-Muller Switching functions with Fixed Polarity", Bulletin of the Faculty of Engineering, Assiut University, VOL.20, NO. 1, 1992, PP. 85-92.
- 8) "On the design of Multiplexer logic networks by Spectral means", AMSE PRESS, VOL.30, NO. 3, 1994, PP. 15-30.
- 9) "Triangle technique for Reed-Muller canonical expansions of Boolean Functions", AMSE PRESS, VOL.28,, NO. 2, 1995, PP. 1-16.
- 10) "Minimization of Ternary logic functions of a single output system", Journal of Science and Technology, VOL.1, NO. 1, 1996, PP. 1-8.
- 11) "One more way to calculate the coefficients of the Reed-Muller vector for

- Completely specified binary functions", PROC. Of INT. AMSE Conference, BRNO, CZ, VOL.1, 1996, PP. 45-49.
- 12) "A probabilistic methodology for Speech Recognition Systems", Journal of Science and Technology, VOL.2, NO. 1, 1997, PP. 1-11.
  - 13) "Canonical Reed-Muller expansions for a group of binary functions", Journal Of Science and Technology, VOL.2, NO. 1, 1997, PP.30-42.
  - 14) "Design of multiple-output AND-XOR networks with fixed polarity(PART I)", Journal of Science and Technology, VOL.3, NO. 1, 1998, PP. 1-21.
  - 15) "On deciding cell boundaries in land mobile cellular systems: a simple and General computer program", JST, VOL.4, NO. 3, 1999, PP. 1-15.
  - 16) "نظام برمجي لتصميم النظم المنطقية الثلاثية", JST, VOL.8, NO. 1, 2003, PP. 16-32.
  - 17) "الوضع المستقبلي للطاقة الكهربائية في القطاع المنزلي في اليمن", JST,VOL.-4, NO. 2, 1999, PP. 1-3.
  - 18) "الإستهلاك المنزلي للطاقة", JST, VOL.4, NO. 3, 1999, PP. 1-7.
  - 19) "Arabic Isolated Digits Recognizer(AIDR) System", JST, VOL.8,NO. 2, 2003, PP. 3-19.
  - 20) "تصميم النظم الرقمية على أساس المداول الرقمي", JST, VOL.11, NO. 1, 2006, PP. 1-22.
  - 21) "An Improved Trellis Decoding Algorithm for Maximum Likelihood Decoding Of Linear Block Codes", JST, VOL. 13, NO. 2, 2008, PP.17-29.
  - 22) "طريقة لتصميم النظم الرقمية على أساس المداول الرقمي", JST,Vol.(17), NO.1, 2012, pp. 16 - 35 .

### 8.2 Books

- 1) " أسس تصميم النظم المنطقية الإلكترونية ", 1995، صنعاء ، اليمن.
- 2) " فن الهندسة الإلكترونية – الجزء 1 " ، 1997 ، صنعاء ، اليمن .
- 3) " المعادلات التفاضلية للعلوم والهندسة " ، 1997 ، صنعاء ، اليمن .
- 4) " المنطق الرقمي الثلاثي وأسس تصميم الحاسوب " ، 2001، صنعاء ، اليمن.
- 5) " شبكات AND - XOR وتصميم النظم المنطقية الإلكترونية " ، 2001 ، صنعاء ، اليمن .
- 6) " الرقم 7 وشمولية الدين الخاتم " ، 2005 ، صنعاء ، اليمن .
- 7) " عظمة الخالق " ، يتم اعداده للنشر .
- 8) " تصميم الحاسوب الرقمي : عتاد وبرمجيات " ، تحت الإعداد .
- 9) " التفاعل المتسلسل ولإتبعات المستحث وتطبيقاتهما العسكرية " ، لم يتم نشره .